

LGIce *Logic Analyzer* *Emulator*

- **Universal Emulator**

- ✓ 64K Code Memory
- ✓ 64K Data Memory
- ✓ 64K Breakpoints
- ✓ Assembly / HLL Trace
- ✓ C-Compiler Support

- **Emulation Pod's**

- ✓ EM8085 Pod & Trainer
- ✓ EM6502 Pod & Trainer
- ✓ EM6809 Pod & Trainer
- ✓ EM8032 Pod & Trainer
- ✓ EM535 Pod & Trainer
- ✓ EM552 Pod & Trainer
- ✓ LPC and many more..

- ✓ Training Manual & CD

- **Universal Trainer Option**

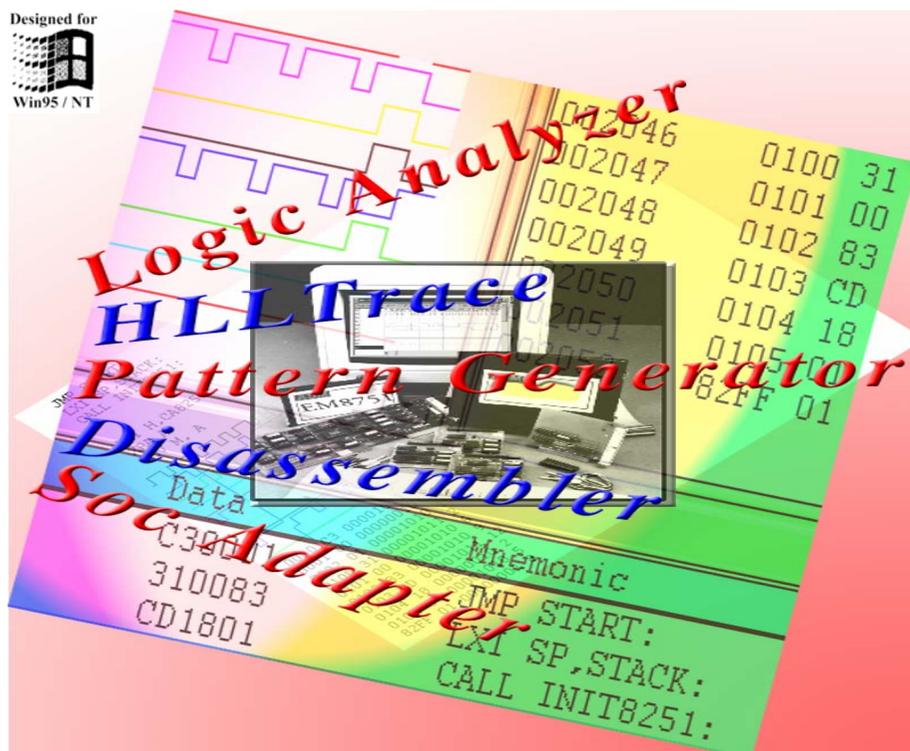
- ✓ µP/CPLD/FPGA add-on
- ✓ Programming cable
- ✓ Parallel/Serial/VGA
- ✓ PS2 Mouse & Keyboard
- ✓ Stepper/ G.P interface
- ✓ 16x2 LCD Display
- ✓ 4 Digit 7-SEG Display
- ✓ 4x4 Array Key's
- ✓ 12 bit ADC

- **Logic Analyzer Option**

- ✓ 100Mhz Speed
- ✓ 32 Channels
- ✓ 128K Memory/Channel
- ✓ Mixed Mode Analysis
- ✓ 65535 Event Counter
- ✓ 65535 Count Delay
- ✓ 16 Trigger Words
- ✓ 4 Level Triggering
- ✓ Pre/Post Delay
- ✓ 3 External Clock(s)
- ✓ Disassembly Pods

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Transforms the engineers desktop into a
Versatile Embedded Design Laboratory



LGIce - Modular Approach, a key to Success

Over a decade ago, ADM introduced its first PC compatible Universal emulator. Since then, more than 3000 such system's has been installed, and has helped many developers in creating successful products.

Our emulation system concept, consists of a Universal base unit, CPU specific emulation module, and a flexible PC interface. Hence even today, the first Universal emulation system can still be upgraded to support the latest microcontroller derivatives, popular Compilers and Microsoft® 32 bit Operating systems. This not only saves money on investment but allows a continuous development of professional applications.

LGIce - designed from the ground-up for Windows® 95/98/XP increases users flexibility by providing a familiar software interface with an enhanced integration of various Emulator and Logic analyzer resources.

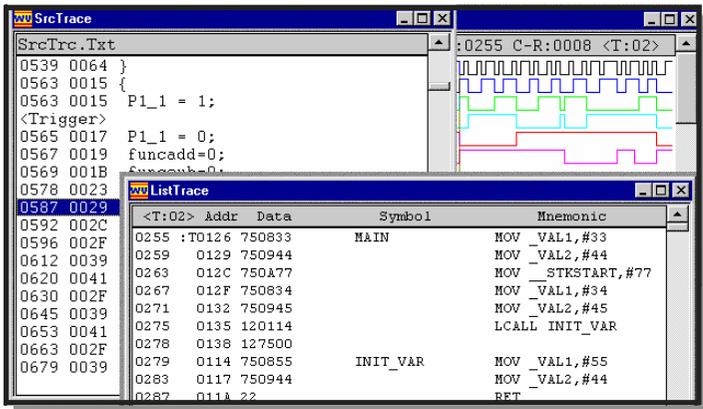
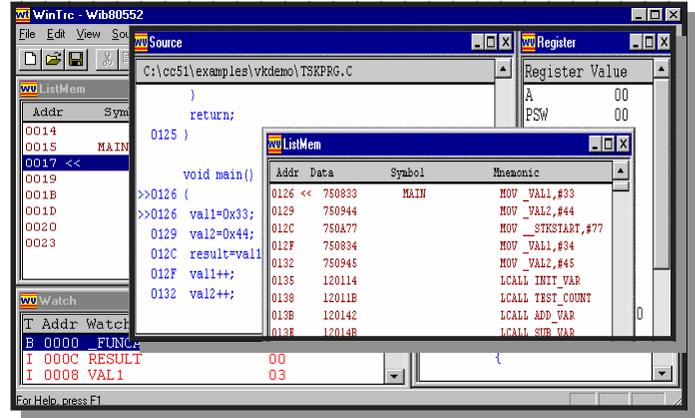


LGIce At High-Level

LGIce marks a new phase for embedded design support where μ C designs are migrating to HLL languages such as C, a trend away from traditional assembly level debugging.

The HLL-Manager shows the HLL source code correlated to the processor's assembly address. Variables pertaining to the HLL program can be examined and modified very easily.

The user can single step, set breakpoints and follow any program flow by a simple mouse click. All user variables and register's are automatically updated. Tool bar controls and floating menus guarantees ease of use.



Excellent Debug Features

Using LGIce, debugging your application efficiently is very simple, as all the standard emulator and logic analyzer functions are now available with parallel port options.

The emulation memory can be freely allocated as both code and data. Thus operation without target is no problem. Large programs can now be downloaded and debugged swiftly.

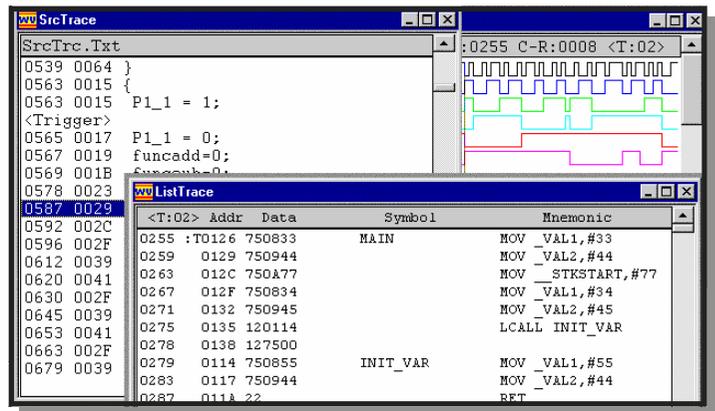
Any numbers of hardware breakpoints can be set across the entire address range at any point you like. A mouse click in the high level language window or assembly window is enough to define or execute upto a breakpoint.

Optional Non-intrusive Real Time Trace

For a look back in time, Logic Analyzer option displays the real time trace buffer in various display modes that help you track down possible errors quickly.

For software developers, this trace buffer is depicted as an assembler instruction or as a high level language trace of C source lines. This Program Flow information can be correlated with external trigger events/signals and can be displayed as graphical signal trace, like an oscilloscope.

The user can collect multiple traces of relevant functions or interrupt calls and compare them for further analysis.



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