

TKBase

Logic Analyzer Hosted
Real Time. Real Learning
Versatile Hardware Design Lab

- **Universal VLSI / μ C Trainer**

- ✓ Multi-Vendor FPGA/CPLD/ μ C
- ✓ PLCC, PQFP, TQFP devices
- ✓ Upto 240 pin devices
- ✓ All I/O with VCC/GND links.
- ✓ Support's 1.8V to 5V devices
- ✓ Vendor Specific Gate density
- ✓ JTAG Programming cable
- ✓ Parallel/Serial/USB/SPI/CAN
- ✓ PS2 Mouse & Keyboard I/F
- ✓ Traffic Light/Stepper interface
- ✓ On board Clock 100K-10Mhz
- ✓ 16x2 LCD Display
- ✓ 4 Digit 7-SEG Display
- ✓ 4x4 Matrix Keypad
- ✓ I2C EEPROM & RTC
- ✓ Serial ADC / DAC with Pot.

- **Pattern Generator (Optional)**

- ✓ 32 Channels
- ✓ Expandable to 48 Channel
- ✓ 128K bit Patterns
- ✓ Single Step Mode
- ✓ Loop Mode

- **Logic Analyzer (Optional)**

- ✓ 100Mhz Speed
- ✓ 32 Channels
- ✓ 128K Memory/Channel
- ✓ Mixed Mode Analysis
- ✓ 65535 Event Counter
- ✓ 65535 Count Delay
- ✓ 16 Trigger Words
- ✓ 4 Level Triggering with Delay
- ✓ 3 External Clock's

- **Daughter Boards (Optional)**

- ✓ **89C51** Daughter Board
- ✓ **ARM7** Daughter Board
- ✓ **CoreMP7** Daughter Board
- ✓ **PIC16F877** Daughter Board
- ✓ **CPLD** Daughter Board
- ✓ **FPGA** Daughter Board
- ✓ **PicoBlaze SoftCore** Board
- ✓ **PIC SoftCore** Board

- ✓ **C Compiler / Debugger**
- ✓ **Real Time Disassembly** Support

- **Complete Courseware**

APPLIED DIGITAL
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PicoBlaze™

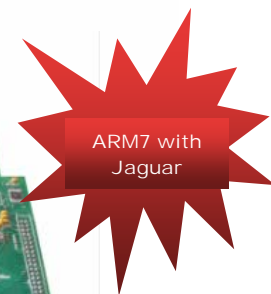
LPC210x
ARM7TDMI-S™

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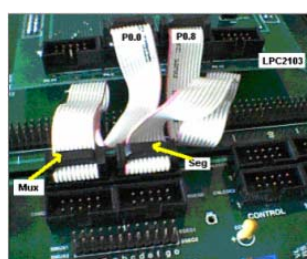


Perfect Educational Solution

TKBase is a PC hosted Universal training tool aimed at enabling engineers to learn complete Digital/ μ C based technology from 89C51 to FPGA /CPLD /PSoC /DSP/ARM7 /ARM9 platform.

TKBase integrates both hardware and software test tools like Logic Analyzer, Pattern Generator, configured μ C Disassembler, Compilers/Assemblers/Debuggers, μ C/FPGA/CPLD/SoC Daughter Boards, all as a single, ready-to-use interactive electronic lab.

TKBase provides the perfect platform to train young engineers. It includes full capabilities necessary for analog and digital hardware design, HDL-based design, microprocessor systems design using both FPGA-hosted and discrete processors, and embedded software development.



TK Base—Universal μ C / FPGA / CPLD / ARM7 Evaluation board !

An integral part of Laboratory training is TKBase - an Universal FPGA/ CPLD/ μ C/ARM7/PSoC -based hardware evaluation kit. TKBase acts as a reconfigurable implementation platform to allow rapid interchangeable and interactive prototype development.

Features:

The TKbase houses a plug-in daughter board containing a target FPGA/ CPLD/ μ C device. Daughter boards can be easily swapped to support training for variety of devices. The universal concept provides support for multi-vendor devices available in PLCC,TQFP,PQFP packages up to 240 pins. Compilers/Debuggers/Placement and programming tools for the target device is handled by vendor specific device tools. These tools are sourced and installed separately.

TKBase also contains a range of off-chip peripherals such as a 4x4 array Keys, LCD display, LED arrays, DIP switches, serial/parallel/VGA/USB ports, ADC/DAC, RTC & general IO connectors etc. TKBase connects to the user's PC and follows JTAG standard to download/debug designs on the target device allowing easy access to various onboard peripherals without the need to build a conventional prototype board.

All I/O lines are taken out through berg header's with jumpered VCC and Ground pins for fast static test. An engineer can now 'burn & learn' any port-connected I/O signal lines and buses in real time using our integrated Pattern Generator & Logic Analyzer.



TK Base

Real Time Stimulation— Hardware Test Bench

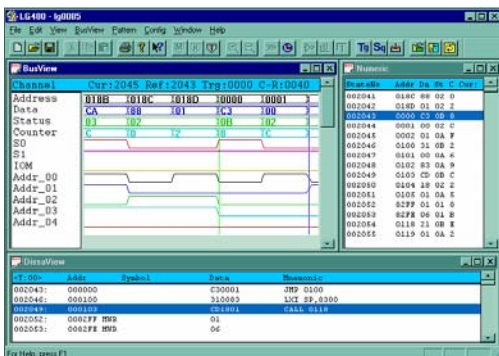
Our pioneering new design technology now brings together the world of Logic Analysis and Pattern Generation in a single, integrated design environment. This new technology enables engineers to have more options than ever before for real-time testing of FPGA/CPLD/ μ C/ARM7/PSoC systems.

Most popular processor cores and its associated compilers, assemblers, linkers and debuggers are fully supported. Processor specific Disassembly adapters provide highly optimized 'C' or Assembly source file trace.

TKBase used in conjunction with LG320's Integrated Pattern Generator's allows us to deliver a system that enables rapid implementation, stimulation and testing of complete digital systems without the need for extensive software synthesis and verification. The verification is of an order of magnitude much faster than just software simulation - based approaches.

The LG320 Logic Analyzer captures live stimulus response, and utilizes the live data to automatically create a test bench. This allows the FPGA/CPLD/ μ C/ARM7/PSoC device cores to be exercised at 'actual hardware speeds'.

LG320's integrated stimulation takes advantage of the reconfigurable nature of TKBase multi device platform, allowing engineers to try multiple design iteration and minimizing the reliance on time-consuming software only simulation.



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